CS 315—Computer Logic and Digital Design  
Fall 2006

Catalog Data:  
Prerequisite: 202 and 215 each with a grade of C or better. Introduction to switching algebra and its applications. Combinational logic and combinational circuit components. Sequential logic and sequential circuit components. Asynchronous sequential circuits.

Textbooks:  

Instructor:  
Dr. Shahram Rahimi  
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Office Hours:  
T-T: 8:30 to 9:30 & 10:50 to 11:50 & 3:15pm to 4:15pm and with appointments

Main topics:  
• An Overview of Computer Organization  
• Switches and Transistors  
• Boolean Algebra and Logic  
• Binary Arithmetic and Number Systems  
• Combinational Logic and Circuits  
• Sequential Logic and Circuits  
• Memory Logic Design  
• Datapath Unit

Grading:  
Test1: 20%, Test2: 20%, Test3: 25%  
Homework and Quizzes: 25%  
Attendance and Participation: 10%  
NOTE: These percentages are tentative; there may be significant changes.

Grading Scale:  
A = 90-100, B = 80-89, C = 70-79, D = 60-69, F < 60.

Missing Exams:  
If you should miss an exam (not including quizzes), you must let me know before the exam. You may call me, send me E-mail, or come by my office to inform me. If the excuse is reasonable, I will replace your exam score with your final exam percentage.

Attendance:  
I strongly encourage regular attendance in my classes and will take attendance at the beginning of each class. You are responsible for all announcements and for all material presented in the lecture sessions.

Turning in work:  
Turn in your work on time. Your works should be turned in before I start the class, unless I extend the deadline.

Final Notes:  
1) Personal laptops and cell phones are not allowed to be used in the class. 2) During the lab sessions, computers should be only used for doing the lab assignments and reading class materials.